Cree® TR5050™ LEDs
CxxxTR5050-Sxx000
Data Sheet

Cree’s TR5050 LEDs are the next generation of solid-state LED emitters that combine highly efficient InGaN materials with Cree’s proprietary device technology and silicon-carbide substrates to deliver superior value for the TV-backlighting and general-illumination markets. The TR5050 LEDs are among the brightest in the top-view market while delivering a low forward voltage, resulting in a very bright and highly efficient solution. The design is optimally suited for industry-standard top-view packages.

FEATURES
• Rectangular LED RF Performance
  – 450 nm – 180 mW min
• Adhesive Die Attach
• Low Forward Voltage – 3.3 V Typical at 120 mA
• Maximum DC Forward Current - 180 mA
• Class 2 ESD Rating
• InGaN Junction on Thermally Conductive SiC Substrate

APPLICATIONS
• Large LCD Backlighting
  – Television
• General Illumination
• Medium LCD Backlighting
  – Portable PCs
  – Monitors
• LED Video Displays
• White LEDs

CxxxTR5050-Sxx000 Chip Diagram

Top View

Die Cross Section

Bottom View

Cathode (-) 98-μm diameter
Anode (+) 90-μm diameter

t = 175 μm
### Maximum Ratings at $T_a = 25^\circ C$ Notes 1&3

<table>
<thead>
<tr>
<th>Part Number</th>
<th>DC Forward Current Note 4</th>
<th>Peak Forward Current (1/10 duty cycle @ 1 kHz)</th>
<th>LED Junction Temperature</th>
<th>Reverse Voltage</th>
<th>Operating Temperature Range</th>
<th>LED Chip Storage Temperature</th>
<th>Recommended Die Sheet Storage Conditions</th>
<th>Electrostatic Discharge Threshold (HBM) Note 2</th>
<th>Electrostatic Discharge Classification (MIL-STD-883E) Note 2</th>
</tr>
</thead>
<tbody>
<tr>
<td>CxxxTR5050-Sx000</td>
<td>180 mA</td>
<td>230 mA</td>
<td>150°C</td>
<td>5 V</td>
<td>-40°C to +100°C</td>
<td>-40°C to +120°C</td>
<td>≤30°C / ≤85% RH</td>
<td>1000 V</td>
<td>Class 2</td>
</tr>
</tbody>
</table>

### Typical Electrical/Optical Characteristics at $T_a = 25^\circ C$, $I_f = 120$ mA Note 3

<table>
<thead>
<tr>
<th>Part Number</th>
<th>Forward Voltage ($V_f$, V)</th>
<th>Reverse Current [$I(V_f=5V)$, μA]</th>
<th>Full Width Half Max ($\lambda_D$, nm)</th>
</tr>
</thead>
<tbody>
<tr>
<td>C450TR5050-Sx000</td>
<td>2.7</td>
<td>3.3</td>
<td>3.5</td>
</tr>
<tr>
<td>C460TR5050-Sx000</td>
<td>2.7</td>
<td>3.3</td>
<td>3.5</td>
</tr>
</tbody>
</table>

### Mechanical Specifications

<table>
<thead>
<tr>
<th>Description</th>
<th>Dimension</th>
<th>Tolerance</th>
</tr>
</thead>
<tbody>
<tr>
<td>P-N Junction Area ($\mu$m)</td>
<td>426 x 443</td>
<td>±35</td>
</tr>
<tr>
<td>Chip Area ($\mu$m)</td>
<td>500 x 500</td>
<td>±35</td>
</tr>
<tr>
<td>Chip Thickness ($\mu$m)</td>
<td>175</td>
<td>±15</td>
</tr>
<tr>
<td>Au Bond Pad Diameter Anode ($\mu$m)</td>
<td>90</td>
<td>±10</td>
</tr>
<tr>
<td>Au Bond Pad Thickness ($\mu$m)</td>
<td>1.0</td>
<td>±0.5</td>
</tr>
<tr>
<td>Au Bond Pad Diameter Cathode ($\mu$m)</td>
<td>98</td>
<td>±10</td>
</tr>
<tr>
<td>Bottom Area ($\mu$m)</td>
<td>327 x 327</td>
<td>±35</td>
</tr>
</tbody>
</table>

**Notes:**

1. Maximum ratings are package-dependent. The above ratings were determined using lamps in chip-on-MCPCB (metal core PCB) packages for characterization. Ratings for other packages may differ. Junction temperature should be characterized in a specific package to determine limitations. Assembly processing temperature must not exceed 325°C (< 5 seconds).
2. Product resistance to electrostatic discharge (ESD) according to the HBM is measured by simulating ESD using a rapid avalanche energy test (RAET). The RAET procedures are designed to approximate the maximum ESD ratings shown.
3. All products conform to the listed minimum and maximum specifications for electrical and optical characteristics when assembled and operated at 120 mA within the maximum ratings shown above. Efficiency decreases at higher currents. Typical values given are within the range of average values expected by manufacturer in large quantities and are provided for information only. All measurements were made using lamps in T-1 3/4 packages (with Hysol OS4000 epoxy encapsulant and clear epoxy die attach). Optical characteristics measured in an integrating sphere using Illuminance E.
4. The maximum forward current is determined by the thermal resistance between the LED junction and ambient. It is crucial for the end-product to be designed in a manner that minimizes the thermal resistance from the LED junction to ambient in order to optimize product performance.
Standard Bins for CxxxTR5050-Sxx000

LED chips are sorted to the radiant flux and dominant wavelength bins shown. A sorted die sheet contains die from only one bin. Sorted die kit (CxxxTR5050-Sxxxx) orders may be filled with any or all bins (CxxxTR5050-xxxx) contained in the kit. All radiant flux and dominant wavelength values shown and specified are at If = 120 mA.

Note: The radiant-flux values above are representative of the die in a Cree 5-mm lamp.
Characteristic Curves

These are representative measurements for the TR5050 LED product. Actual curves will vary slightly for the various radiant flux and dominant wavelength bins.
Radiation Pattern

This is a representative radiation pattern for the TR5050 LED product. Actual patterns will vary slightly for each chip.